

Invited Lecture



Dr. Debashis Dutta

Title: Overview of Indian Electronics sector and Policy initiatives

Abstract: Globally Electronics (including Information Technology) is recognized as a meta-resource. This sector has been accorded highest priority in the “Make in India” and “Digital India” programmes of the Government. Apart from economic imperative, security concerns warrant special focus on Electronic System Design & Manufacturing right up to chip level design and fabrication. An overview of Indian electronics industry scenario, global statistics and policy initiatives taken for an inclusive growth and development of the sector are discussed.

Biography: Dr. Debashis Dutta obtained his M.Tech. from IIT Delhi and PhD from IIT Kharagpur. His research interests include low-power analog circuit design, biomedical electronics and neural networks. He has been in the committee of reviewers for IEEE TCAS-II.

He had worked in M/s Semiconductor Complex Ltd., Chandigarh, as an R&D Engineer in Wafer Fab during the year 1981 to 1983. Subsequently, he joined the Wireless Planning and Coordination Wing of the Ministry of Communication, Govt. of India. He belongs to the 1981 batch of the Indian Engineering Services.

During 1984, he joined the then Department of Electronics now being called Ministry of Electronics & Information Technology, where he had worked in various capacities. During 2007 to 2010, he headed the Industrial Promotion - Electronics & Hardware Manufacturing Division of and was instrumental in bringing out the Report of the Task Force to suggest measure to stimulate the growth of IT, ITES and Electronics hardware manufacturing industry in India.

From 2010 to 2016, he coordinated the R&D in IT & Electronics Group and also directed the Special Manpower Development Program in VLSI Design. He has held several important positions like Director General, CMET; Director General, CDAC; Director in the board of NICSII etc. He initiated some major programs like National Supercomputing Mission; Development of Microprocessor; Development of 1.5T MRI; Development of 6 MeV LINAC; Development of Medical Isotopes using 15 MeV LINAC; Development of NaVIC chip set and receivers; Special Manpower Development program for Chip 2 System; Development of Secure phone; Development of various cyber security tools and technologies; Digital Preservation; Porting of devanagiri fonts on smart phone and its deployment in Micromax smart phone; Development of super capacitors; and Setting up of a pilot plant for e-Waste (PCB) processing plant.

He superannuated from Ministry of Electronics and IT in 31/12/2017. Currently, he is providing consultancy to various academic institutions and Electronics manufacturing industries.