



# MNNIT ALLAHABAD

Call for Papers

## VICAS 2018



Springer

### International Conference on VLSI, Communication and Signal Processing

November 29 to December 1, 2018

Conference Website: [www.mnnit.ac.in/vcas2018/](http://www.mnnit.ac.in/vcas2018/)

Organized by: Electronic & Communication Engineering Department MNNIT Allahabad

The Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology (MNNIT) ALLAHABAD, proudly announces the hosting of **First International Conference on VLSI, Communication and Signal Processing (VICAS 2018)**. The conference aims to provide a platform for academicians, individuals working in the industry, as well as students to showcase their research innovations and get recognition. VICAS 2018 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

#### TRACKS

##### VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit, Low-Power Design, Packaging & Interconnect, 2D material based Devices, MEMS & NEMS design and Technology, Devices Modeling and Process simulation, Thin Film, Ultra-High-Speed Transistors, HEMT/HBT Reconfigurable Architecture Design, Power Devices and Reliability, Artificial Intelligence (Process & Device), Internet of Things (Process & Device), Emerging Technology.

##### COMMUNICATION

Communication Theory and Applications, Optical Networks and Systems Microwave Communications, Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks, IOT and Sensor Networks, Antenna Design for Communication Systems and Networks, Vehicular Communications and Networks, Wireless Modeling/Algorithms, and Simulation, Cognitive Radio and Spectrum Management, Adaptive Communication Systems and Networks, Intelligent Protocols and Designing for Communication Systems and Networks.

##### SIGNAL PROCESSING

Signal Processing: Theory, Methods and Applications, Signal Processing Systems and Architectures, Signal Processing Algorithms, Image/Video and Multidimensional Signal processing, Multimedia Signal Processing Biomedical Signal Processing and Bioinformatics, Signal Processing for Communication and Networking, Signal Processing for Machine Learning and Compression, Signal Processing for Smart systems, Sparse Signal Processing.

#### Registration Fee

Category	Indian Delegates	Foreign Delegates
Student	INR 5,000/-	USD 250
Faculty	INR 8,000/-	USD 400
Industry	INR. 10,000/-	USD 500

#### IMPORTANT DATES

Submission date	August 15, 2018
Acceptance notification	September 15, 2018
Registration starts	September 16, 2018
Deadline for final paper	November 1, 2018
Conference date	November 29 to December 1, 2018

**Paper Submissions:** Prospective authors are encouraged to submit research papers containing the original contributions which have not been published or submitted elsewhere. Springer guidelines can be obtained of conference website for manuscript preparation.

**Easy Chair link for paper submission: -**

<https://easychair.org/conferences/?conf=vcas18>

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## Track Chairs

VLSI	Dr. Sanjeev Rai, MNNIT Allahabad Dr. Santosh Kumar Vishvakarma, IIT Indore
Communication	Dr. Yogendra Kumar Prajapati, MNNIT Allahabad Dr. Rajat Singh, IIIT Allahabad
Signal Processing	Dr. Basant Kumar, MNNIT Allahabad Dr. Rajib Kumar Jha, IIT Patna

## Publications:

Select papers from the conference will be published by Springer as a proceedings book volume. Springer will conduct quality checks on the accepted papers and only papers that pass these checks will be published.

## CONTACTS INFORMATION

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