

Publications

Journal

1. M. Sarkar, P. Ghosal, and S.P. Mohanty, “Minimal Reversible Circuit Synthesis on a DNA Computer”. in Natural Computing, Springer, Volume 16, Issue 3, pp. 463-472, September 2017.
2. M. Sarkar, P. Ghosal, and S.P. Mohanty, “Exploring the Feasibility of a DNA Computer: Design of an ALU Using Sticker-Based DNA Model”. in IEEE Transactions on NanoBioscience, Volume 16, No. 6, pp. 383-399, Sept. 2017.

Conference Paper

1. M. Sarkar, P. Ghosal, and S.P. Mohanty, “Reversible Circuit Synthesis using ACO and SA based Quine-McCluskey method” in 2013 IEEE 56th International Midwest Symposium on Circuits and Systems (MWSCAS), Columbus, OH, 2013, pp. 416-419.
2. M. Sarkar and P. Ghosal, “Implementing Data Structure Using DNA: An Alternative in Post CMOS Computing” in 2015 IEEE Computer Society Annual Symposium on VLSI (ISVLSI), Montpellier, 2015, pp. 345-349.
3. P. Chatterjee, M. Sarkar and P. Ghosal, “Computing in Ribosomes: Performing Boolean Logic Using mRNA-Ribosome System” in 2016 IEEE Computer Society Annual Symposium on VLSI (ISVLSI), Pittsburgh, PA, 2016, pp. 260-265.

Book Chapter

1. P. Ghosal, M. Sarkar, and P. Chatterjee, “A New Paradigm towards Performance Centric Computation beyond CMOS: DNA Computing”, in Nano-CMOS and PostCMOS Electronics: Vol 2. Circuits and Design, The Institute of Engineering and Technology (IET), Chapter 12, pp. 379-408, DOI: 10.1049/PBCS030E_ch12.