



**सूक्ष्म कणिका एवं संचार अभियंत्रण विभाग**  
**Department Of Electronics & Communication Engineering**  
**मोतीलाल नेहरू राष्ट्रीय प्रौद्योगिकी संस्थान**  
**MOTILAL NEHRU NATIONAL INSTITUTE OF TECHNOLOGY**  
**इलाहाबाद— 211004**  
**ALLAHABAD-211004**

Advt. No.: 02/ECED/C2S/Project Staff/2023

Dt.: 01-12-2023

**Advertisement for recruitment of Project Staffs under Chips to Start-up (C2S) Project**

<b>Project Title</b>	<i>Design and Fabrication of Digitally Assisted CMOS Transmitter Frontend for Indian Long Range (i-LoRa) Application</i>	
<b>Sponsoring Agency</b>	Ministry of Electronics & Information Technology, R&D Electronics Group (Microelectronics Development Division), Govt. of India	
<b>Name of Position</b>	Research Associate (RA) & Project Associate (PA)	
<b>Duration (In Years)</b>	Initially for 12 months, extendable up to 05 Years or the duration of the Project whichever is earlier.	
<b>Number of Positions</b>	02 (RA) + 01 (PA)	
<b>Essential Educational Qualification and Experience</b>	(i) B.E./ B.Tech./ or equivalent degree in Electronics & Communication Engineering - <b>for Project Associate (PA): Desirable - GATE score</b> (ii) M.E./M.Tech. in Microelectronics & VLSI Design or related Specializations with valid GATE score – <b>for Research Associate (RA)</b>	
<b>Age</b>	Should not exceed 28 years as on the last date of application.	
<b>Age Relaxation</b>	05 years is applicable for Female/SC/ST/PwD candidate	
<b>Desired Skills</b>	(i) Knowledge of OS (Window/Linux/CentOS/RHEL), Server handling, network file system, knowledge of Cadence/Synopsys/Mentor Graphics/TCAD, etc. (ii) Digital/Analog/RF/Mixed-mode Circuit Design, HDL Coding	
<b>Emoluments</b>	Rs. 35,000/- consolidated( <b>for RA</b> ); Rs. 29,000/- consolidated( <b>for PA</b> ) per month + <b>10% increment per annum</b> on satisfactory performance	
<b>Accommodation</b>	The selected candidates are eligible for on-campus Hostel & Mess facilities on payment basis subject to availability.	
<b>Job Nature</b>	Execute the objectives of research project. (i.e. Design and Fabrication of Digitally Assisted CMOS Transmitter Frontend for Indian Long Range (i-LoRa) Application and demonstration of the prototype of the same. )	
<b>Other Benefits</b>	The selected candidates are eligible to register for MTech/PhD degree at MNNIT Allahabad as per institute rules. However He/She needs to apply for the same when the advertisement is open.	
<b>Application Mode</b>	The Performa of application is attached with this advertisement. Candidates are required to take the print of the same and upload it along with other supporting documents to the link given here: <a href="https://forms.gle/6FtM7gXtSe84Ensq9">https://forms.gle/6FtM7gXtSe84Ensq9</a>	
<b>Last Date To Apply</b>	<b>28 December 2023</b>	
<b>Contacts</b>	Dr. Rajendra Kumar Nagaria, Professor [HAG] Chief Investigator, C2S Project ECE Department MNNIT Allahabad Email: <a href="mailto:smdp@mnnit.ac.in">smdp@mnnit.ac.in</a>	Dr. Santosh Kumar Gupta Associate Professor Co-Chief Investigator, C2S Project ECE Department MNNIT Allahabad Email: <a href="mailto:smdp@mnnit.ac.in">smdp@mnnit.ac.in</a>
<b>Contact Number</b>	0532-2271475; +91 8004928398	

**General Terms & Conditions:**

- 1 The position is purely temporary
- 2 The CI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
- 3 The candidate is required to enclose a brief resume/ curriculum vitae/ bio-data along with the filled application form.
- 4 Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
- 5 The candidate is expected to join immediately, if selected.
- 6 No TA/DA will be paid for attending the interview

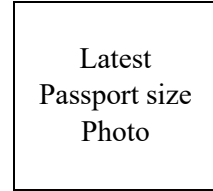
**APPLICATION FOR THE POSITION OF RA/PA UNDER  
CHIPS TO START-UP (C2S) PROJECT  
MOTILAL NEHRU NATIONAL INSTITUTE OF TECHNOLOGY ALLAHABAD  
PRAYAGRAJ – 211004, UTTAR PRADESH, INDIA**

**Advt. No.: 02/ECED/C2S/Project Staff/2023**

**Dated:** \_\_\_\_\_

Note: Please upload a copy of bio-data/ resume/ curriculum-vitae with supporting documents;  
Link for upload: <https://forms.gle/6FtM7gXtSe84E.nsq9>

1. **Name of the candidate** :
2. **Father's name** :
3. **Date of Birth (DD/MM/YYYY)** :
4. **Sex (Male / Female)** :
5. **Marital status (Married / Unmarried)** :
6. **Category (OC/OBC/SC/ST/EWS/PwD)** :
7. **Position Applied for** :
8. **Corresponding Address** :
9. **Mobile Number & Email** :
10. **Educational qualifications** :



Degree	Specialization/ subjects	Board/ University	Year of passing	Class	Percentage/ CGPA
High School					
Intermediate					
B.E./B.Tech.					
M.E./M.Tech.					
Any other					

**11. GATE score earned/ qualified**

Specialization	Year appeared	Score	All India rank

**12. Research/Industrial/Teaching experience**

S.No.	Organisation/ Industry served	Date/ Month of Entry	Date/ Month of Leaving	Months/ Years of Experience

**13. Journal / Conference Publications (attach a separate page with DOI)**

**14. Any other relevant information**

**Declaration**

I ..... hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm, that I am available for ONLINE interview, if necessary.

**Date :**

**Signature of the candidate**